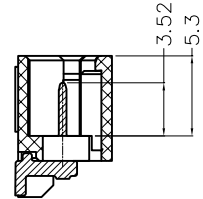
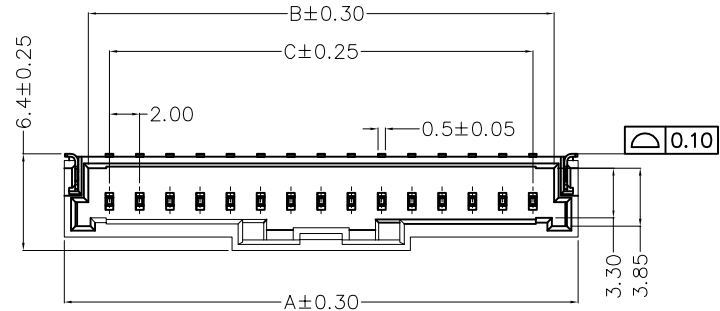


REV	LOCATIONS	DESCRIPTION	DATE	REVISER	APPD
1	△	Update Temperature Range-Operating	12/SEP/24	MATT	LEO

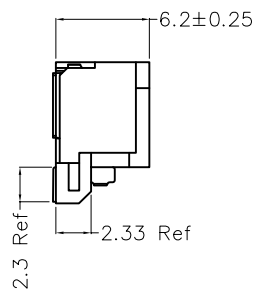
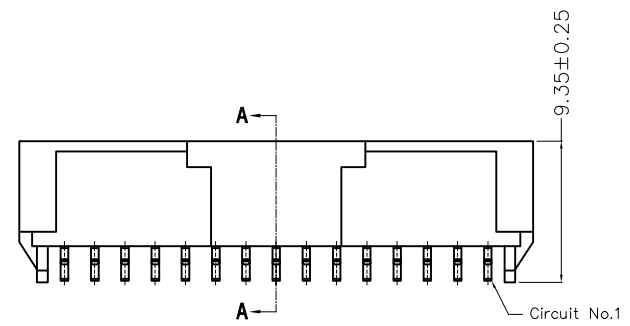
SPECIFICATIONS

Electrical  
 Current Rating: 2.0A AC(rms)/DC  
 Voltage Rating: 125V AC(rms)/DC  
 Contact Resistance: 40 mΩ Max  
 Insulation Resistance: 1000 MΩ MIN  
 Withstanding Voltage: 500V AC r.m.s  
 Temperature Range-Operating: -40°C ~ +105°C △

Material and Plating  
 Housing: PA9T( UL 94V-0)  
 Contact Pin: Brass  
 Plating: Tin Plated/Gold Flash



Section A-A

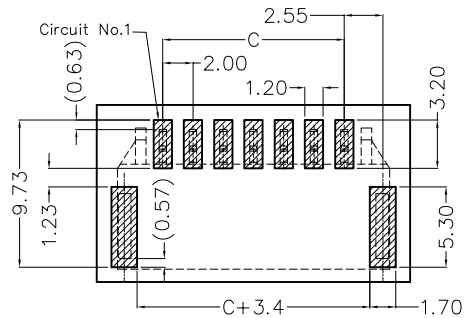


Circuits (n)	Part No.	Dimensions(mm)		
		A	B	C
2	FWF20029-S02BX5NAM	8.00	4.80	2.00
3	FWF20029-S03BX5NAM	10.00	6.80	4.00
4	FWF20029-S04BX5NAM	12.00	8.80	6.00
5	FWF20029-S05BX5NAM	14.00	10.80	8.00
6	FWF20029-S06BX5NAM	16.00	12.80	10.00
7	FWF20029-S07BX5NAM	18.00	14.80	12.00
8	FWF20029-S08BX5NAM	20.00	16.80	14.00
9	FWF20029-S09BX5NAM	22.00	18.80	16.00
10	FWF20029-S10BX5NAM	24.00	20.80	18.00
11	FWF20029-S11BX5NAM	26.00	22.80	20.00
12	FWF20029-S12BX5NAM	28.00	24.80	22.00
13	FWF20029-S13BX5NAM	30.00	26.80	24.00
14	FWF20029-S14BX5NAM	32.00	28.80	26.00
15	FWF20029-S15BX5NAM	34.00	30.80	28.00

Ordering Information

FWF 200 29 - S XX B X 5 NA M  
 1 2 3 4 5 6 7 8 9 10

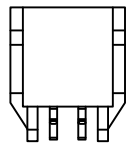
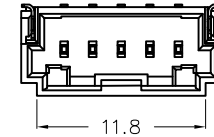
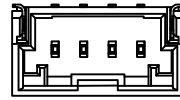
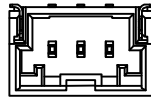
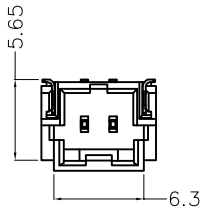
1 Category FWF-Wafer	2 Series Number 200-Pitch2.0mm	3 Distinction No. 29	4 Row Option S-Single Row	5 Circuits XX	6 Entry Angle B-90° Angl
7 Plating 1-Gold Flash 2-Tin Plated	8 Material-Resin 5-PA9T	9 Color-Resin NA-Natural	10 Packaging M-Reel		



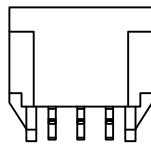
Recommended P.C.Board Layout  
 General Tolerances: ±0.05mm

<p>THIRD ANGLE PROJECTION</p>	GENERAL TOLERANCES (UNLESS SPECIFIED)		APPROVE BY LEO	DATE 08/AUG/22	PART NO. FWF20029-SXXBX5NAM	ITEM NO. FWF20029	<p>Building Technology Cornerstone</p>	
	X. ±0.30	X.* ±5°	CHECKED BY GISELLE	DATE 08/AUG/22	TITLE Wire to Board (Wafer) Pitch 2mm 90° Angl (SMT)			REV 0
	X.X ±0.20	X.X* ±2°	DRAWN BY MATT	DATE 08/AUG/22	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO TXGA INDUSTRIAL ELECTRONICS(S.Z)CO.,LTD AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION			SHEET NO. 1/4
	X.XX ±0.15	X.XX* ±1°						
SCALE 5:1	SIZE A4	X.XXX ±0.10	X.XXX* ±0.5°					

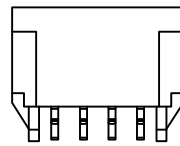
REV	LOCATIONS	DESCRIPTION	DATE	REVISER	APPD



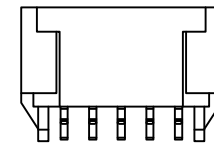
2Circuits



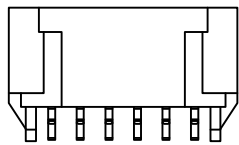
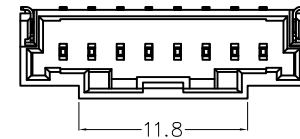
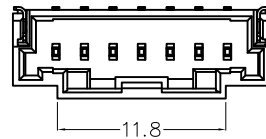
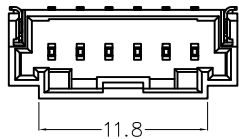
3Circuits



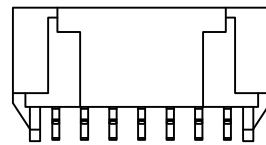
4Circuits



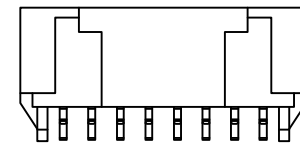
5Circuits



6Circuits



7Circuits



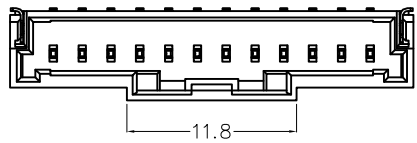
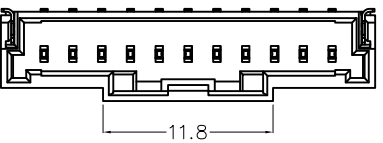
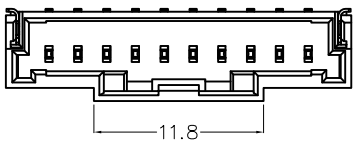
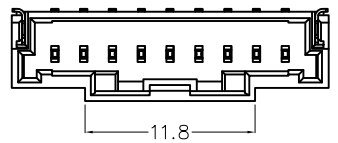
8Circuits

<p>THIRD ANGLE PROJECTION</p>	GENERAL TOLERANCES (UNLESS SPECIFIED)		APPROVE BY LEO	DATE 08/AUG/22	PART NO. FWF20029-SXXBX5NAM	ITEM NO. FWF20029	<p>Building Technology Cornerstone</p>	
	X. ±0.30	X.* ±5°	CHECKED BY GISELLE	DATE 08/AUG/22	TITLE Wire to Board (Wafer) Pitch 2mm 90° Angl (SMT)			REV 0
	DESIGN UNITS METRIC(mm)	X.X ±0.20	X.X* ±2°	DRAWN BY MATT	DATE 08/AUG/22	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO TXGA INDUSTRIAL ELECTRONICS(S.Z)CO.,LTD AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION		SHEET NO. 2/4
	SCALE 5:1	SIZE A4	X.XX ±0.15	X.XX* ±1°	X.XXX ±0.10	X.XXX* ±0.5°		

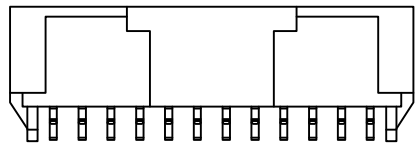
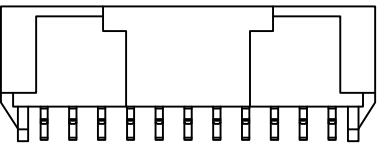
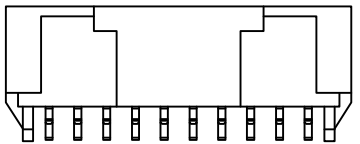
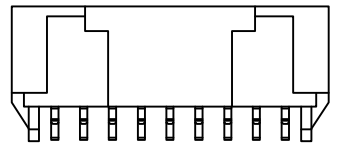
1 2 3 4 5 6 7 8

REV	LOCATIONS	DESCRIPTION	DATE	REVISER	APPD

A



B



C

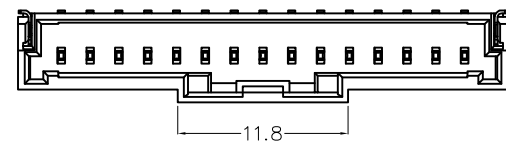
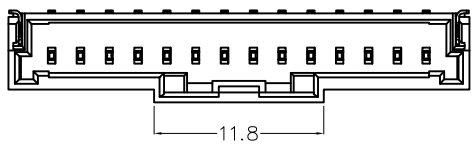
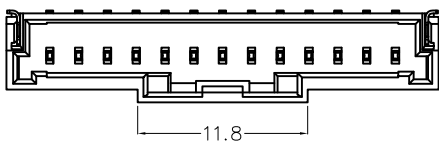
9Circuits

10Circuits

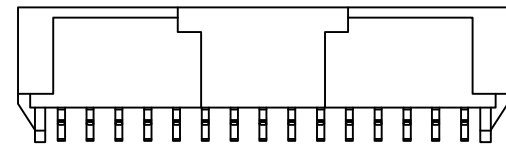
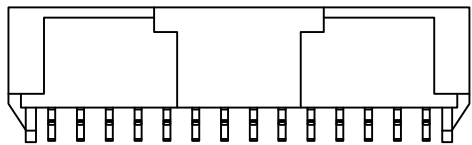
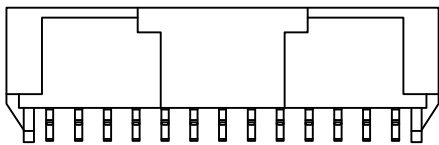
11Circuits

12Circuits

D



E



13Circuits

14Circuits

15Circuits

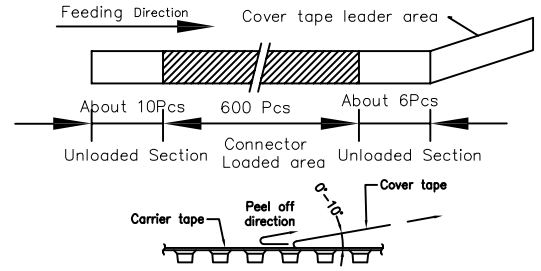
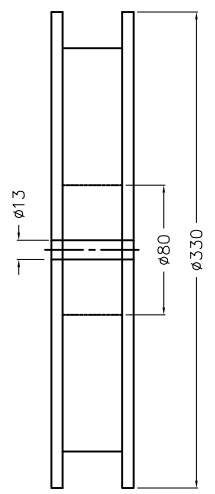
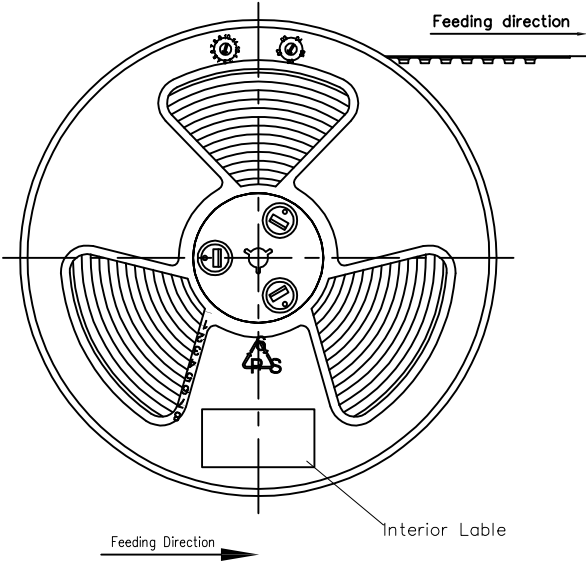
F

<p>THIRD ANGLE PROJECTION</p>	GENERAL TOLERANCES (UNLESS SPECIFIED)		APPROVE BY	DATE	PART NO.	ITEM NO.	<p>Building Technology Cornerstone</p>
	X. ±0.30	X.* ±5°	LEO	08/AUG/22	FWF20029-SXXBX5NAM	FWF20029	
	X.X ±0.20	X.X* ±2°	CHECKED BY	DATE	TITLE		
	X.XX ±0.15	X.XX* ±1°	GISELLE	08/AUG/22	Wire to Board (Wafer) Pitch 2mm 90° Angl (SMT)		
SCALE	SIZE	X.XXX ±0.10	X.XXX* ±0.5°	DRAWN BY	DATE	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO TXGA INDUSTRIAL ELECTRONICS(S.Z)CO.,LTD AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION	
5:1	A4			MATT	08/AUG/22	REV 0	SHEET NO. 3/4

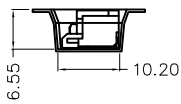
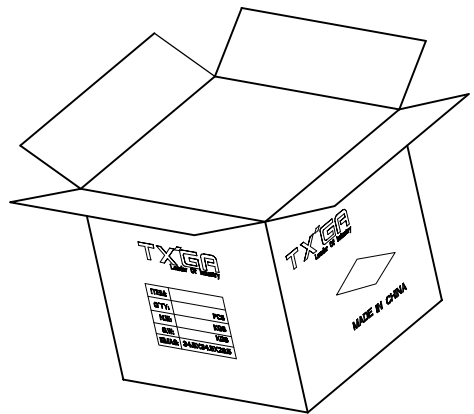
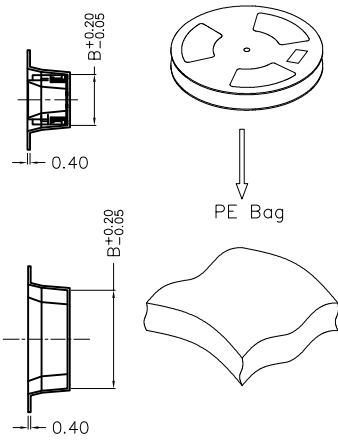
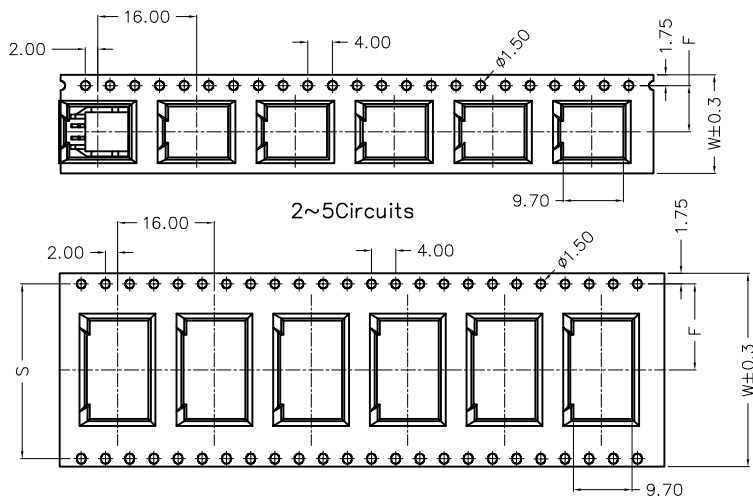
1 2 3 4 5 6 7 8

REV	LOCATIONS	DESCRIPTION	DATE	REVISER	APPD

- Note:
- 10 sprocket hole cumulative tolerance  $\pm 0.2$
  - Carrier camber is within 1.0mm in 100mm
  - Material: White Conductive polystyrene Alloy 100% recyclable
  - All dimensions meet EIA-481 requirements
  - Component load per 13" reel: 600 pcs



Circuits (n)	Dimensions(mm)				Circuits (n)	Dimensions(mm)			
	W	S	F	B		W	S	F	B
2	16	-	7.5	8.25	9	44	40.9	20.2	22.25
3	24	-	11.5	10.25	10	44	40.9	20.2	24.25
4	24	-	11.5	12.25	11	44	40.9	20.2	26.25
5	24	-	11.5	14.25	12	44	40.9	20.2	28.25
6	32	28.9	14.2	16.25	13	44	40.9	20.2	30.25
7	32	28.9	14.2	18.25	14	44	40.9	20.2	32.25
8	32	28.9	14.2	20.25	15	56	52.9	26.2	34.25



 THIRD ANGLE PROJECTION DESIGN UNITS METRIC(mm) SCALE 5:1 SIZE A4	GENERAL TOLERANCES (UNLESS SPECIFIED)		APPROVE BY	DATE	PART NO.	ITEM NO.	 Building Technology Cornerstone REV 0 SHEET NO. 4/4 THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO TXGA INDUSTRIAL ELECTRONICS(S.Z)CO.,LTD AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION
	X. $\pm 0.30$	X.* $\pm 5^\circ$	LEO	08/AUG/22	FWF20029-SXXBX5NAM	FWF20029	
	X.X $\pm 0.20$	X.X* $\pm 2^\circ$	CHECKED BY	DATE	TITLE		
	X.XX $\pm 0.15$	X.XX* $\pm 1^\circ$	GISELLE	08/AUG/22	Wire to Board (Wafer) Pitch 2mm 90° Angl (SMT)		
X.XXX $\pm 0.10$	X.XXX* $\pm 0.5^\circ$	DRAWN BY	DATE				
		MATT	08/AUG/22				